

PATENT NUMBER

O.I.P.E.

PATENT DATE

SCANNED BY 1 a. CH

TS Douglas Wallace

APPLICANT'S

TITLE

Computer system and printed circuit board manufactured in accordance with a quasi-Monte Carlo simulation technique for multi-dimensional spaces

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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